

FAST RECOVERY RECTIFIER

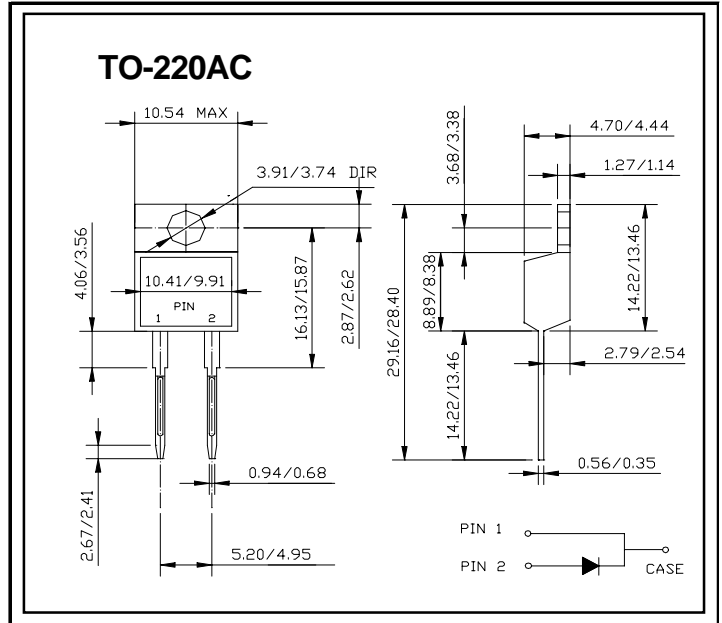
VOLTAGE RANGE: 50 --- 600 V
CURRENT: 8.0 A

FEATURES

- ◇ Low cost
- ◇ Diffused junction
- ◇ Low leakage
- ◇ Low forward voltage drop
- ◇ High current capability
- ◇ Easily cleaned with Freon, Alcohol, Isopropanol and similar solvents
- ◇ The plastic material carries U/L recognition 94V-0

MECHANICAL DATA

- ◇ Case: JEDEC TO-220AC, molded plastic
- ◇ Terminals: Solderable per MIL-STD-202, Method 208
- ◇ Weight: 0.064 ounces, 1.81 gram
- ◇ Mounting position: Any



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load. For capacitive load, derate by 20%.

		FR 801	FR 802	FR 803	FR 804	FR 805	UNITS
Maximum recurrent peak reverse voltage	V_{RRM}	50	100	200	400	600	V
Maximum RMS voltage	V_{RMS}	35	70	140	280	420	V
Maximum DC blocking voltage	V_{DC}	50	100	200	400	600	V
Maximum average forward rectified current @ $T_c=100^\circ\text{C}$	$I_{F(AV)}$	8.0					A
Peak forward surge current 8.3ms single half-sine-wave superimposed on rated load @ $T_j=125^\circ\text{C}$	I_{FSM}	150					A
Maximum instantaneous forward voltage @ 8.0 A	V_F	1.3					V
Maximum reverse current @ $T_A=25^\circ\text{C}$ at rated DC blocking voltage @ $T_A=100^\circ\text{C}$	I_R	10 150					μA
Maximum reverse recovery time (Note1)	t_{rr}	150				250	ns
Typical junction capacitance (Note2)	C_J	70					pF
Typical thermal resistance (Note3)	$R_{\theta JA}$	22					$^\circ\text{C/W}$
Operating junction temperature range	T_J	- 55 --- + 175					$^\circ\text{C}$
Storage temperature range	T_{STG}	- 55 --- + 175					$^\circ\text{C}$

NOTE:1. Measured with $I_F=0.5\text{A}$, $I_R=1\text{A}$, $I_{rr}=0.25\text{A}$.

2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance from junction to ambient.

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FIG.1 – FORWARD DERATING CURVE

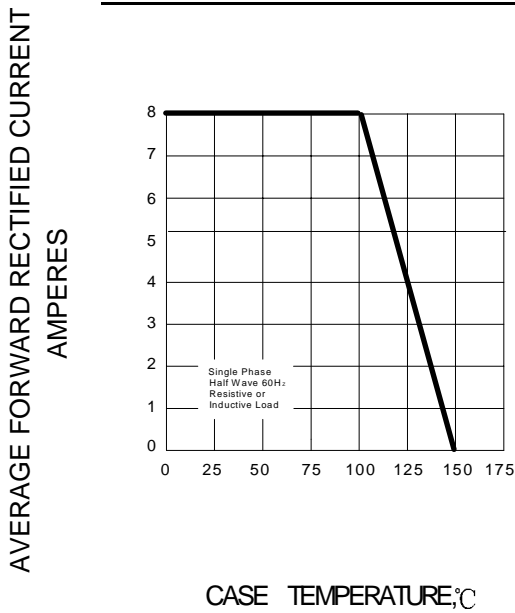


FIG.2-PEAK FORWARD SURGE CURRENT

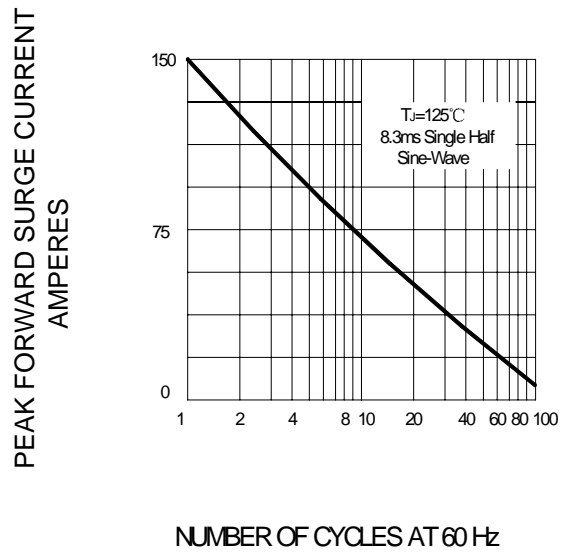


FIG.3-TYPICAL JUNCTION CAPACITANCE

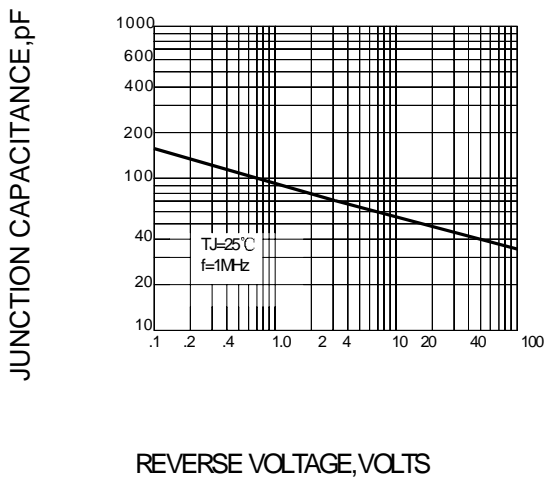


FIG.4 –TYPICAL FORWARD CHARACTERISTIC

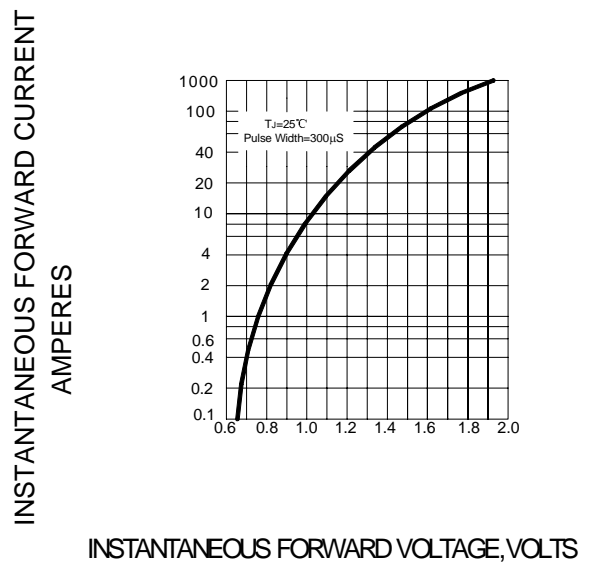


FIG.5 – REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

